

File No. 1266/
11/12/17

Office of the Dean
FACULTY OF ENGINEERING & TECHNOLOGY
Jamia Millia Islamia, New Delhi-110025

December 11, 2017

ADMISSION NOTICE
Ph.D. ADMISSIONS-2017, DEPT. OF ELECTRONICS & COMM. ENGG.

Enclosed herewith is the list of candidates provisionally selected for admission to Ph.D. Program in the **Department of Electronics & Communication Engineering**. Selected candidates are required to report in the Office of the Dean, Faculty of Engineering & Technology, on December 15, 2017 during working hours for verification/submission of their documents and also to collect the Offer Letters for admission. **Candidates are required to bring the following documents (in original) along with a set of self-attested photocopy of each of the documents:**

- Admit card
- Admission Form
- Five copies of the recent passport size photograph
- All degrees, certificates, mark-sheets (from class Xth onwards)
- Proof of age based on the high school/secondary school examination
- Character Certificate
- Migration Certificate (in original) must be provided within one month of the date of completion of admission, failing which admission is liable to be cancelled.
- A Consent Letter from the Co-Supervisor(s) specifying his/her field of specialization with full correspondence address.
- Proof of Mailing/Permanent Address as filled in the application form: (Any one of the following: Passport, Aadhar Card, Driving License, Voter ID Card, Bank Passbook, Electricity Bill, Telephone (Landline) Bill, Water Charges Bill, Certificate issued by Gram Pradhan, Certificate issued by Municipal Corporation.)
- Certificate from an MBBS doctor or government hospital certifying blood group.
- All Experience Certificates, if any
- No Objection Certificate (NOC) from the current employer, if the candidate is employed.
- If the candidate is unemployed, an Undertaking on Non-Judicial Stamp Paper of Rs. 10/- duly attested by Notary Public or Oath Commissioner.
- Muslim OBC/ST candidates will be required to submit the respective category certificate issued by the competent authority.
- In addition to this, Muslim OBC/ST candidates will be required to submit the certificate of not belonging to creamy layer, and an Affidavit declaring the same
- For candidates who have applied in PWD category, certificate for disability is required from a government certified authority.




- Candidates seeking admission under 'Kashmiri migrant' category will be required to submit a certificate in support of their claim of being Kashmiri migrant obtained from the registered divisional commissioner of the concern state/ union territory.
- Candidates seeking admission under 'Candidates from Jammu & Kashmir' Category will be required to submit a state domicile certificate in support of their claim of being from J&K.
- In case an Intervening/Gap period is involved, the candidate has to submit a certificate/affidavit from Class I gazetted officer/Notary Public for the entire intervening/gap period showing year-wise pre occupation and non-indulgence in any criminal activities after leaving the institution last attended.
- Affidavit for not indulging in ragging activity as per prescribed performa from candidate and guardian, issued by Oath Commissioner or First Class Magistrate.



(Professor Mehtab Alam)
Dean



Dept. of Elec. & Comm. Engg.
 428779
 07/12/17

List of Provisionally Selected Candidates for Admission to PhD Program in the Department of Electronics & communication

S.No.	Roll No.	Name	Fathers Name	Broad Research Interest/Area	Supervisor	Co-Supervisor
1	P4538045	Satya Prakash Singh	Brij Kishor Singh	Design and Analysis of Efficient Beamforming Technique for MIMO-OFDM in Wireless Sensor Networks	Dr. S.A. Imam	
2	R4538006	Suvarna Mujumdar	Sharad Ram Subhedar	Design and Development of CNT Based Digitally Programmable Signal Processing Circuit	Dr. Neelofer Afzal	
3	R4538008	Anam Khan	Inayat Ullah Khan	Design & Development of Emerging Energy Efficient Nano-Electronic Devices	Dr. S.A. Ioan	
4	R4538018	Ravendra Singh	Ram Kirti Singh	Design and Modeling of Tunnel FET Based modern Analog Building Blocks and its application	Dr. Dinesh Prasad	

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7/12/17

HEAD
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